

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants	:	Taylor et al.	)	I hereby certify that this paper is
			)	being filed electronically with the
U.S. Serial No.	:	10/829,495	)	U.S. Patent and Trademark Office
			)	on this date:
Filed	:	April 22, 2004	)	
			)	
Title	:	"Method and System for Fail-	)	
		Safe Renaming of Logical	)	
		Circuit Identifiers for Rerouted	)	<b>July 23, 2009</b>
		Logical Circuits in a Data	)	
		Network "	)	
			)	
			)	
			)	<b>/Felipe Hernandez/</b>
Art Unit	:	2419	)	Felipe Hernandez
			)	Registration No. 61,971
Examiner	:	Ashley L. Shivers	)	Attorney for Applicants

**SUPPLEMENTAL RESPONSE TO**  
**THE FINAL OFFICE ACTION DATED MARCH 23, 2009**  
**ACCOMPANYING REQUEST FOR CONTINUED EXAMINATION (RCE)**  
**PURSUANT TO 37 CFR § 1.114**

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Please enter the following amendments and consider the accompanying remarks.

**Amendments to the claims** are reflected in the listing of the claims beginning on page 2 of this paper.

**Remarks** begin on page 8 of this paper.